



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Daniel Xu et al.

§ Art Unit: 2812

Serial No.: 10/633,881

§ Examiner: Alexander G. Ghyka

Filed: August 4, 2003

§ Atty Docket: ITO.0024US
P14742

For: Forming Planarized
Semiconductor Structures

§ Assignee: Intel Corporation

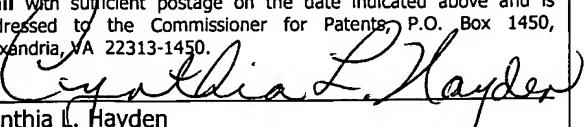
§

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the restriction requirement mailed March 2, 2005, please amend the above-referenced patent application as follows:

Date of Deposit: <u>March 11, 2005</u>
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Cynthia L. Hayden